

## Department of Computer Engineering

**Journal Index Sheet**

## Academic Year: 2018-19

**Subject: System Software Lab**

**Class: TE Sem: VI**

## Course Code: CSL602

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **Sr.**  **No.** | **Title** | **Page No.** | **DOP** | **DOC** | **Grade** | **Signature of Faculty** |
|  | **Experiments** |  |  |  |  |  |
| 1. | To Implement pass1 of 2 pass assembler | 1 | 06-02-19 | 27-02-19 | A |  |
| 2. | To Implement pass2 of 2 pass assembler | 7 | 13-02-19 | 27-02-19 | A |  |
| 3. | Design and implementation of macro processor | 13 | 27-02-19 | 13-03-19 | A |  |
| 4. | Design and implementation of a lexical analyser | 19 | 13-03-19 | 20-03-19 | A |  |
| 5. | Use of tool lex | 29 | 13-03-19 | 20-03-19 | A |  |
| 6. | Use of tool yacc | 37 | 03-04-19 | 03-04-19 | A |  |
| 7. | Design and implementation of Operator Precedence parser | 43 | 27-03-19 | 03-04-19 | B |  |
| 8. | Creating 3 address code using LEX and YACC | 51 | 03-04-19 | 03-04-19 | A |  |
| 9. | Creating your own library in c and study gcc compilation process | 59 | 13-03-19 | 20-03-19 | A |  |
| 10. | Grey box probing | 67 | 03-04-19 | 03-04-19 | A |  |
|  | **Assignments** |  |  |  |  |  |
| 1. | Assignment 1 | 71 | 05-02-19 | 06-02-19 | A |  |
| 2. | Assignment 2 | 79 | 01-04-19 | 03-04-19 |  |  |

***Grades: A-Excellent, B-Good, C-Fair, D-Poor, DOP – Date of Performance, DOC – Date of Completion***

**K. J. Somaiya Institute of Engineering and Information Technology Sion(E), Mumbai-400022**

## Department of Computer Engineering

**Journal Index Sheet**

**AcademicYear: 2018-19**

**Subject: System Software Lab**

**Class: TE Sem:VI**

## Course Code: CSL602

**Student Name: Bhavenkumar Chetan Naik**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Experiments** | | | | | |
| **Assessment Outcome** | **Participation** | **Organization and Preparation** | **Accuracy and Neatness of journal** | **Time Management and Punctuality** | **Total** |
| **Exp\_1** | 5 | 4 | 4 | 5 | 18 |
| **Exp\_2** | 5 | 4 | 4 | 5 | 18 |
| **Exp\_3** | 5 | 4 | 5 | 4 | 18 |
| **Exp\_4** | 5 | 4 | 5 | 4 | 18 |
| **Exp\_5** | 5 | 4 | 5 | 4 | 18 |
| **Exp\_6** | 5 | 4 | 5 | 4 | 18 |
| **Exp\_7** | 4 | 5 | 4 | 4 | 17 |
| **Exp\_8** | 4 | 5 | 4 | 5 | 18 |
| **Exp\_9** | 5 | 4 | 5 | 4 | 18 |
| **Exp\_10** | 4 | 5 | 4 | 5 | 18 |
| **Assignments** | | | | | |
| **Assign.\_1** | 5 | 4 | 4 | 5 | 18 |
| **Assign.\_2** |  |  |  |  |  |
| **Overall Average** | | | | | 18 |
| **Marks to Grade** | | | | | A |

**Term Work Certification**

This is to certify that Mr. /~~Miss~~ Bhavenkumar Chetan Naik Roll No. 41 Class/Div TE/C of Branch Computer Engineering has satisfactorily completed the specified term work in subject System Software Lab Subject Code CSL602 Lab No. 502 , K. J. Somaiya Institute of Engineering and Information Technology, Sion(E), Mumbai as required by the University of Mumbai during the academic year 2018-2019.



## Faculty Incharge HOD